

REMARKS

Claims 1-77 have been canceled without prejudice and claims 78-84 have been added and are pending in the application. Applicant requests examination of such pending claims.

Respectfully submitted,

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Application Serial No. 10/044,206
Filing Date January 11, 2002
Inventor Jerome Michael Eldridge
Assignee Micron Technology, Inc.
Group Art Unit 2812
Examiner Laura M. Schillinger
Attorney's Docket No. MI22-1914
Title: Methods for Forming Void Regions, Dielectric Regions and Capacitor Constructions

**VERSION WITH MARKINGS TO SHOW CHANGES MADE ACCOMPANYING
PRELIMINARY AMENDMENT****In the Specification**

The replacement specification paragraphs incorporate the following amendments. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

CROSS REFERENCE TO RELATED APPLICATION

This patent resulted from a divisional application of U.S. Patent Application Serial No. 09/651,815, filed August 30, 2000, entitled "Methods of Forming Void Regions, Dielectric Regions and Capacitor Constructions," naming Jerome Michael Eldridge as inventor, which is a continuation of U.S. Patent Application Serial No. 09/146,117, filed September 2, 1998, now U.S. Patent No. 6,140,200, the disclosures of which are incorporated by reference.

In the Claims

The claims have been amended as follows. Underlines indicate insertions and ~~strikeouts~~ indicate deletions.

There are no amendments to the claims.

Claims 1-77 have been canceled without prejudice and claims 78-84 have been added.

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